

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	46	((allocat\$3) with (process\$3 application task\$3 job) with ((cpu processor (process\$3 adj unit)) with (time cycle consum\$5 usage)) with (percent\$3 ratio shar\$3 portion number) with (((processor adj2 set) (processor adj2 group\$3) nod\$2 server))))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/28 15:24
L2	105	((allocat\$3) with (process\$3 application task\$3 job) with ((multiprocessor cpu processor (process\$3 adj unit)) with (time cycle consum\$5 usage)) with (percent\$3 ratio shar\$3 portion number) with (((processor multiprocessor cpu nod\$2 server (processing adj unit)) with (group\$3 set))))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/28 15:54
L3	5	11 and 718/100.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/28 15:55
L4	4	11 and 718/102.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/28 15:55
L5	0	11 and 718/103.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/28 15:55
L6	1	11 and 718/107.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/28 15:55
L7	8	12 and 718/100.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/28 15:55
L8	8	12 and 718/102.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/28 15:55

L9	4	12 and 718/103.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/28 15:55
L10	3	12 and 718/107.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/28 15:55

**Inventor Name Search Result**

Your Search was:

Last Name = DOROFEEV

First Name = ANDREI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10661413	Not Issued	20	09/12/2003	Processor specific dispatching in a heterogeneous configuration	DOROFEEV, ANDREI
09843426	Not Issued	71	04/25/2001	Apparatus and method for scheduling processes on a fair share basis	DOROFEEV, ANDREI V.
10768303	Not Issued	30	01/29/2004	Mechanism for associating resource pools with operating system partitions	DOROFEEV, ANDREI V.
10771827	Not Issued	30	02/03/2004	Multi-level computing resource scheduling control for operating system partitions	DOROFEEV, ANDREI V.

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
<input type="text" value="DOROFEEV"/>	<input type="text" value="ANDREI"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

PALM INTRANET

Day : Friday
 Date: 10/28/2005
 Time: 16:04:04

Inventor Name Search Result

Your Search was:

Last Name = TUCKER

First Name = ANDREW

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>08675236</u>	<u>5937187</u>	150	07/01/1996	METHOD AND APPARATUS FOR EXECUTION AND PREEMPTION CONTROL OF COMPUTER PROCESS ENTITIES	TUCKER, ANDREW
<u>60290057</u>	Not Issued	159	05/10/2001	Fruit/vegetable paring apparatus	TUCKER, ANDREW A.
<u>60590336</u>	Not Issued	159	07/23/2004	Filter	TUCKER, ANDREW C.
<u>09843426</u>	Not Issued	71	04/25/2001	Apparatus and method for scheduling processes on a fair share basis	TUCKER, ANDREW G.
<u>10661413</u>	Not Issued	20	09/12/2003	Processor specific dispatching in a heterogeneous configuration	TUCKER, ANDREW G.
<u>10744360</u>	Not Issued	30	12/22/2003	System logging within operating system partitions	TUCKER, ANDREW G.
<u>10761622</u>	Not Issued	30	01/20/2004	Two-level service model in operating system partitions	TUCKER, ANDREW G.
<u>10762066</u>	Not Issued	30	01/20/2004	System statistics virtualization for operating system partitions	TUCKER, ANDREW G.
<u>10762067</u>	Not Issued	20	01/20/2004	System accounting for operating system partitions	TUCKER, ANDREW G.
<u>10763147</u>	Not Issued	20	01/21/2004	Global visibility controls for operating system partitions	TUCKER, ANDREW G.
<u>10766094</u>	Not Issued	20	01/27/2004	Interprocess communication within operating system partitions	TUCKER, ANDREW G.
<u>10767003</u>	Not Issued	30	01/28/2004	Virtual system console for virtual application environment	TUCKER, ANDREW G.
<u>10767118</u>	Not Issued	20	01/28/2004	Mechanism for implementing a sparse file system for an operating system partition	TUCKER, ANDREW G.
<u>10767235</u>	Not Issued	20	01/28/2004	Mechanism for selectively providing mount information to processes running within operating system partitions	TUCKER, ANDREW G.
<u>10768303</u>	Not Issued	30	01/29/2004	Mechanism for associating resource pools with operating system partitions	TUCKER, ANDREW G.

<u>10769415</u>	Not Issued	30	01/30/2004	Fine-grained privileges in operating system partitions	TUCKER, ANDREW G.
<u>10771698</u>	Not Issued	30	02/03/2004	Multi-level resource limits for operating system partitions	TUCKER, ANDREW G.
<u>10771827</u>	Not Issued	30	02/03/2004	Multi-level computing resource scheduling control for operating system partitions	TUCKER, ANDREW G.
<u>10771870</u>	Not Issued	20	02/04/2004	Login methods for virtual application environments	TUCKER, ANDREW G.
<u>10790988</u>	Not Issued	30	03/02/2004	Virtual processor sets	TUCKER, ANDREW G.
<u>10791023</u>	Not Issued	20	03/02/2004	Interposing library for page size dependency checking	TUCKER, ANDREW G.
<u>60469558</u>	Not Issued	159	05/09/2003	Operating system virtualization	TUCKER, ANDREW G.
<u>08767353</u>	<u>6223204</u>	150	12/18/1996	USER LEVEL ADAPTIVE THREAD BLOCKING	TUCKER, ANDREW G.
<u>08879150</u>	<u>6151639</u>	150	06/19/1997	SYSTEM AND METHOD FOR REMOTE OBJECT INVOCATION	TUCKER, ANDREW G.
<u>08879151</u>	<u>5808911</u>	150	06/19/1997	SYSTEM AND METHOD FOR REMOTE OBJECT RESOURCE MANAGEMENT	TUCKER, ANDREW G.
<u>08884978</u>	<u>6138251</u>	150	06/30/1997	METHOD AND SYSTEM FOR RELIABLE REMOTE OBJECT REFERENCE MANAGEMENT	TUCKER, ANDREW G.
<u>09058406</u>	<u>6185695</u>	150	04/09/1998	METHOD AND APPARATUS FOR TRANSPARENT SERVER FAILOVER FOR HIGHLY AVAILABLE OBJECTS	TUCKER, ANDREW G.
<u>09059406</u>	Not Issued	161	04/14/1998	WASHING MACHINE	TUCKER, ANDREW G.
<u>06703797</u>	<u>4583590</u>	150	02/21/1985	SLIP RELEASING APPARATUS	TUCKER, ANDREW J.
<u>07100303</u>	<u>4813486</u>	150	09/23/1987	RETRACTABLE SLIP ASSEMBLY	TUCKER, ANDREW J.
<u>07883632</u>	<u>5314014</u>	150	05/04/1992	PACKER AND VALVE ASSEMBLY FOR TEMPORARY ABANDONMENT OF WELLS	TUCKER, ANDREW J.
<u>08396410</u>	<u>5685574</u>	150	02/28/1995	SNAP LATCH SEAL LOCATOR FOR SEALINGLY LATCHING TUBING TO A PACKER IN A WELLBORE	TUCKER, ANDREW J.
<u>08918213</u>	<u>5988285</u>	150	08/25/1997	ZONE ISOLATION SYSTEM	TUCKER, ANDREW J.
<u>10455204</u>	Not	93	06/05/2003	COOLED INSULATION SURFACE	TUCKER, ANDREW R.

	Issued			TEMPERATURE CONTROL SYSTEM	
<u>10457594</u>	Not Issued	41	06/09/2003	Actively cooled ceramic thermal protection system	TUCKER, ANDREW R.

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
<input type="text" value="TUCKER"/>	<input type="text" value="ANDREW"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)



[Subscribe \(Full Service\)](#) [Register \(Limited Service, Free\)](#) [Login](#)

Search: ☒ The ACM Digital Library ☐ The Guide

(allocate <near/4> (process or application or task or job) <near/4> ((cpu or pro...

THE ACM DIGITAL LIBRARY

Terms used

allocate near/4 process or application or task or job near/4 cpu or processor process near/2 unit near/4 time or cycl

Sort results by

Display results

[Save results to a Binder](#)

[Search Tips](#)

☐ [Open results in a new window](#)

Results 41 - 60 of 200

Result page: [previous](#) [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#)

Best 200 shown

41 [A Survey of Energy Efficient Network Protocols for Wireless Networks](#)

Christine E. Jones, Krishna M. Sivalingam, Prathima Agrawal, Jyh Cheng Chen
September 2001 **Wireless Networks**, Volume 7 Issue 4

Publisher: Kluwer Academic Publishers

Full text available: [pdf\(271.55 KB\)](#)

Additional Information: [full c](#)

Wireless networking has witnessed an explosion of interest from consumers in recent years for its appl communication infrastructure, energy efficiency will be an important design consideration due to the lir Since the network interface is a significant consumer o ...

Keywords: energy efficient design, low-power design, mobile computing, network protocols,, power a

42 [Building a distributed full-text index for the web](#)

July 2001 **ACM Transactions on Information Systems (TOIS)**, Volume 19 Issue 3

Publisher: ACM Press

Full text available: [pdf\(651.72 KB\)](#)

Additional Information: [full c](#)

We identify crucial design issues in building a distributed inverted index for a large collection of Web pa construction time. We also propose a storage scheme for creating and managing inverted files using ar inverted indexes. Finally, we present pe ...

Keywords: Distributed indexing, Embedded databases, Inverted files, Pipelining, Text retrieval

43 [Compiler-based I/O prefetching for out-of-core applications](#)

Angela Demke Brown, Todd C. Mowry, Orran Krieger
May 2001 **ACM Transactions on Computer Systems (TOCS)**, Volume 19 Issue 2

Publisher: ACM Press

Full text available: [pdf\(499.03 KB\)](#)



Additional Information: [full c](#)

Current operating systems offer poor performance when a numeric application's working set does not f onerous task of rewriting an application to use explicit I/O operations (e.g., read/write). In this paper, and requires only minima ...

Keywords: compiler optimization, prefetching, virtual memory

44



[Data and memory optimization techniques for embedded systems](#)

-  P. R. Panda, F. Catthoor, N. D. Dutt, K. Danckaert, E. Brockmeyer, C. Kulkarni, A. Vandercappelle, P. G. I.
April 2001 **ACM Transactions on Design Automation of Electronic Systems (TODAE)**
Publisher: ACM Press
Full text available:  [pdf\(339.91 KB\)](#) Additional Information: [full c](#)

We present a survey of the state-of-the-art techniques used in performing data and memory-related op one or more out of three important cost metrics: area, performance, and power dissipation of the resul cover a broad spectrum of optimizati ...



Keywords: DRAM, SRAM, address generation, allocation, architecture exploration, code transformation file, size estimation, survey

45 Symbiotic jobscheduling for a simultaneous multithreaded processor

-  Allan Snavey, Dean M. Tullsen
November 2000 **ACM SIGOPS Operating Systems Review , ACM SIGARCH Computer Arch
languages and operating systems ASPLOS-IX**, Volume 34 , 28 Issue 5 , 5
Publisher: ACM Press
Full text available:  [pdf\(247.10 KB\)](#) Additional Information: [full c](#)



Simultaneous Multithreading machines fetch and execute instructions from multiple instruction streams hardware to support simultaneous execution, the operating system scheduler must choose the set of jc that are coscheduled by the operating system jobsche ...

46 An analysis of operating system behavior on a simultaneous multithreaded architecture

-  Joshua A. Redstone, Susan J. Eggers, Henry M. Levy
November 2000 **ACM SIGARCH Computer Architecture News , ACM SIGOPS Operating Sy
languages and operating systems ASPLOS-IX**, Volume 28 , 34 Issue 5 , 5
Publisher: ACM Press
Full text available:  [pdf\(227.80 KB\)](#) Additional Information: [full c](#)

This paper presents the first analysis of operating system execution on a simultaneous multithreaded (mode execution. However, many of the applications most amenable to multithreading technologies spe execution and measurement of the operating sy ...

47 System architecture directions for networked sensors

-  Jason Hill, Robert Szewczyk, Alec Woo, Seth Hollar, David Culler, Kristofer Pister
November 2000 **ACM SIGOPS Operating Systems Review , ACM SIGARCH Computer Arch
languages and operating systems ASPLOS-IX**, Volume 34 , 28 Issue 5 , 5
Publisher: ACM Press
Full text available:  [pdf\(299.01 KB\)](#) Additional Information: [full c](#)


Technological progress in integrated, low-power, CMOS communication devices and sensors makes a ri environment like smart dust. The missing elements are an overall system architecture and a methodok design a tiny event-driven operating system, and sh ...

48 Symbiotic jobscheduling for a simultaneous mutlithreading processor

-  Allan Snavey, Dean M. Tullsen
November 2000 **ACM SIGPLAN Notices**, Volume 35 Issue 11
Publisher: ACM Press
Full text available:  [pdf\(1.31 MB\)](#) Additional Information: [full c](#)

Simultaneous Multithreading machines fetch and execute instructions from multiple instruction streams hardware to support simultaneous execution, the operating system scheduler must choose the set of jc that are coscheduled by the operating system jobsche ...

49 An analysis of operating system behavior on a simultaneous multithreaded architecture

-  Joshua A. Redstone, Susan J. Eggers, Henry M. Levy
November 2000 **ACM SIGPLAN Notices**, Volume 35 Issue 11

Publisher: ACM Press

Full text available:  [pdf\(1.56 MB\)](#)

Additional Information: [full c](#)

This paper presents the first analysis of operating system execution on a simultaneous multithreaded (mode execution. However, many of the applications most amenable to multithreading technologies spe execution and measurement of the operating sy ...

50 System architecture directions for networked sensors




Jason Hill, Robert Szewczyk, Alec Woo, Seth Hollar, David Culler, Kristofer Pister

November 2000

ACM SIGPLAN Notices, Volume 35 Issue 11

Publisher: ACM Press

Full text available:  [pdf\(1.32 MB\)](#)

Additional Information: [full c](#)

Technological progress in integrated, low-power, CMOS communication devices and sensors makes a ri environment like smart dust. The missing elements are an overall system architecture and a methodok design a tiny event-driven operating system, and sh ...



51 Scalable molecular dynamics for large biomolecular systems

Robert K. Brunner, James C. Phillips, Laxmikant V. Kale

November 2000

Proceedings of the 2000 ACM/IEEE conference on Supercomputing (CD

Publisher: IEEE Computer Society

Full text available:  [pdf\(211.89 KB\)](#)  [Publisher Site](#)

Additional Information: [full c](#)

We present an optimized parallelization scheme for molecular dynamics simulations of large biomolecu spatial decomposition scheme, and an aggressive measurement-based predictive load-balancing frame summarizes the broad methodology we are pu ...


52 Process migration



September 2000

ACM Computing Surveys (CSUR), Volume 32 Issue 3

Publisher: ACM Press

Full text available:  [pdf\(1.24 MB\)](#)

Additional Information: [full c](#)

Process migration is the act of transferring a process between two machines. It enables dynamic load c efforts, migration has not achieved widespread use. With the increasing deployment of distributed syst research and product development. As hi ...

Keywords: distributed operating systems, distributed systems, load distribution, process migration

53 Techniques for obtaining high performance in Java programs



Iffat H. Kazi, Howard H. Chen, Berdenia Stanley, David J. Lilja

September 2000

ACM Computing Surveys (CSUR), Volume 32 Issue 3

Publisher: ACM Press

Full text available:  [pdf\(816.13 KB\)](#)

Additional Information: [full c](#)

This survey describes research directions in techniques to improve the performance of programs writte portability of programs. A Java interpreter dynamically executes Java bytecodes, which comprise the ir compilation, possibly at the expense of portabili ...

Keywords: Java, Java virtual machine, bytecode-to-source translators, direct compilers, dynamic com

54 Improving interactive performance using TIPME




Yasuhiro Endo, Margo Seltzer

June 2000

ACM SIGMETRICS Performance Evaluation Review , Proceedings of the '00, Volume 28 Issue 1

Publisher: ACM Press

Full text available:  [pdf\(1.05 MB\)](#)

Additional Information: [full c](#)

On the vast majority of today's computers, the dominant form of computation is GUI-based user interaction. The user's perception of performance is affected by unexpectedly long delays. However, most performance-critical parts of the system, they do little ...

Keywords: interactive performance, monitoring

55 Recency-based TLB preloading

Ashley Saulsbury, Fredrik Dahlgren, Per Stenström
May 2000

Publisher: ACM Press

Full text available:  pdf(651.05 KB)

Additional Information: full_c

Caching and other latency tolerating techniques have been quite successful in maintaining high memory hit rates. However, as memory access patterns are growing beyond the capacity of TLBs. This work presents one of the first attempts to hide TLB miss latency by using a cache of TLB entries. This work presents one of the first attempts to hide TLB miss latency by using a cache of TLB entries. This work presents one of the first attempts to hide TLB miss latency by using a cache of TLB entries.

56 System-level power optimization: techniques and tools

Luca Benini, Giovanni de Micheli
April 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAE:**

Publisher: ACM Press

Full text available: pdf(385.22 KB)

Additional Information: full c

This tutorial surveys design methods for energy-efficient system-level design. We consider electronic systems, namely computation, communication, and storage units, and we review methods of reducing their energy consumption. This survey ...

57 Session summaries from the 17th symposium on operating systems principle (SOSP'99)

 Jay Lepreau, Eric Eide
April 2000 **ACM SIGOPS Operating Systems Review**, Volume 34 Issue 2

Publisher: ACM Press

Full text available: pdf(3.15 MB)

Additional Information: full citation, index terms

58 HPFBench: a high performance Fortran benchmark suite

Y. Charlie Hu, Guohua Jin, S. Lennart Johnsson, Dimitris Kehagias, Nadia Shalaby
March 2000 **ACM Transactions on Mathematical Software (TOMS)**, Volume 26 Issue 1

Publisher: ACM Press

Full text available: pdf(274.52 KB)

Additional Information: full c

The high performance Fortran (HPF) benchmark suite HPFBench is designed for evaluating the HPF language application kernels that reflect the computational structure and communication patterns in fluid dynamics. The benchmark suite consists of 10 kernels, each of which is designed to measure a specific aspect of the HPF language, such as FLOP count, memory usage, communication patterns, etc.

Keywords: benchmarks, compilers, high performance Fortran

59 Borrowed-virtual-time (BVT) scheduling: supporting latency-sensitive threads in a general-purpos

 Kenneth J. Duda, David R. Cheriton
December 1999 **ACM SIGOPS Operating Systems Review**, Proceedings of the seventeen

Publisher: ACM Press

Full text available: pdf(1.81 MB)

Additional Information: full c

Systems need to run a larger and more diverse set of applications, from real-time to interactive to batch specialized to complex real-time paradigms, limiting their applicability to general-purpose systems. In the applications yet weighted sharing ...




The interactive performance of SLIM: a stateless, thin-client architecture

Brian K. Schmidt, Monica S. Lam, J. Duane Northcutt

December 1999

ACM SIGOPS Operating Systems Review , Proceedings of the seventeen

Publisher: ACM Press

Full text available:  [pdf\(1.79 MB\)](#)

Additional Information: [full c](#)

Taking the concept of thin clients to the limit, this paper proposes that desktop machines should just be dedicated interconnection fabric --- much in the same way as a building's telephone services are accessed transparently resume their work on any desktop c ...

Results 41 - 60 of 200

Result page: [previous](#)

The ACM Portal is published by the A
[Terms of Usage](#)

Useful downloads:  [Adobe Acrobat](#)